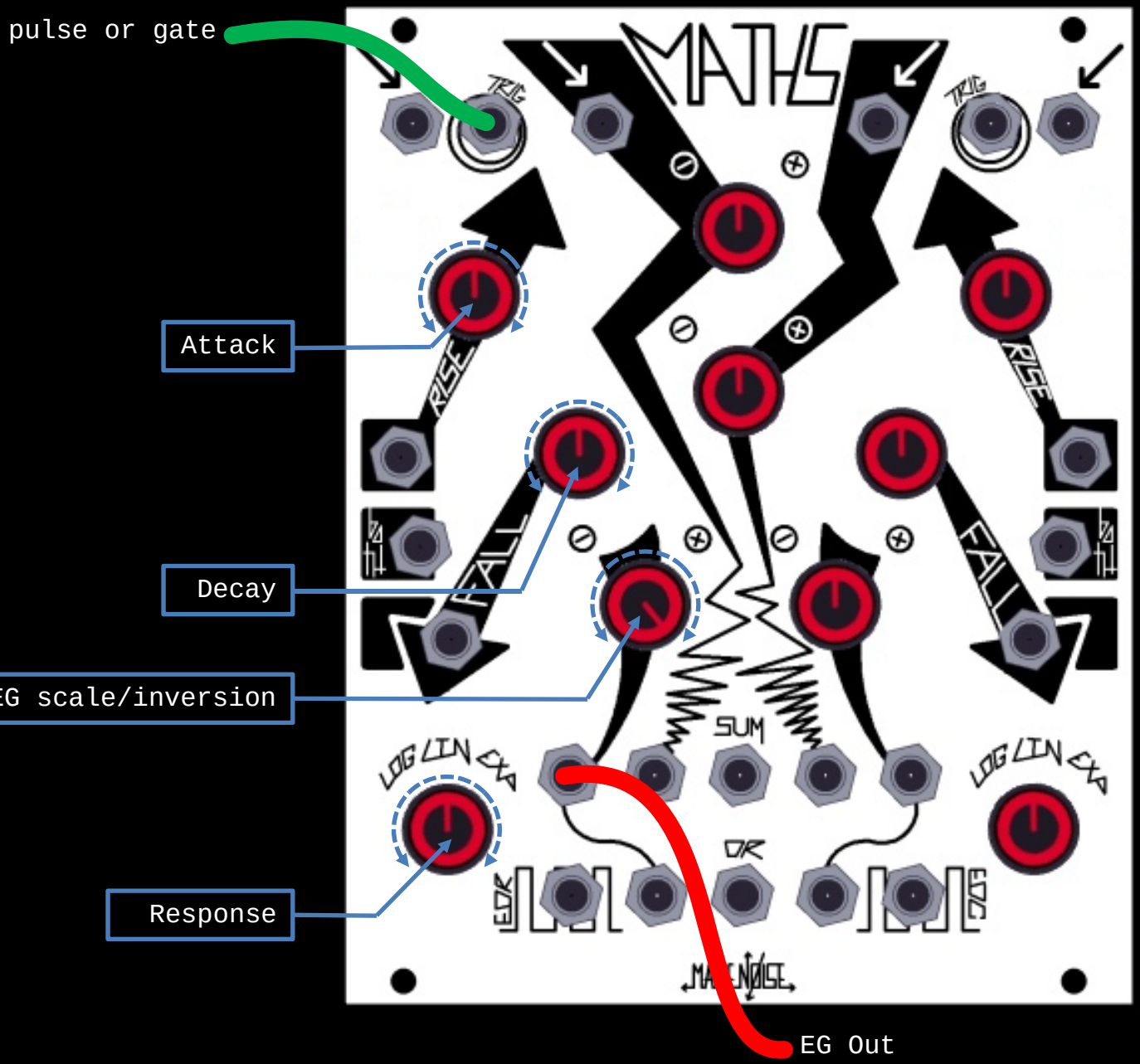
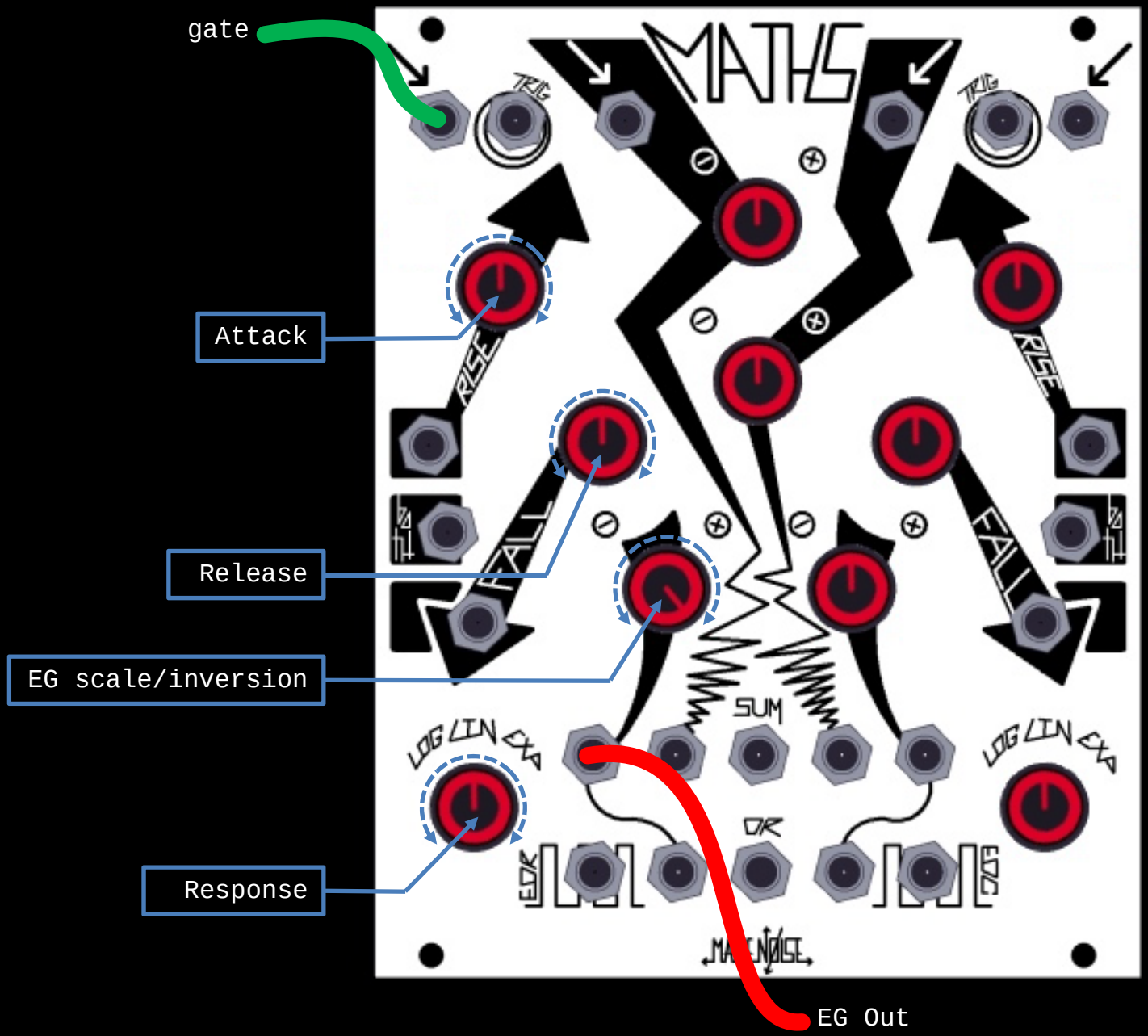
Makenoise Maths
Manual Patch Ideas illustrated

by Demonam

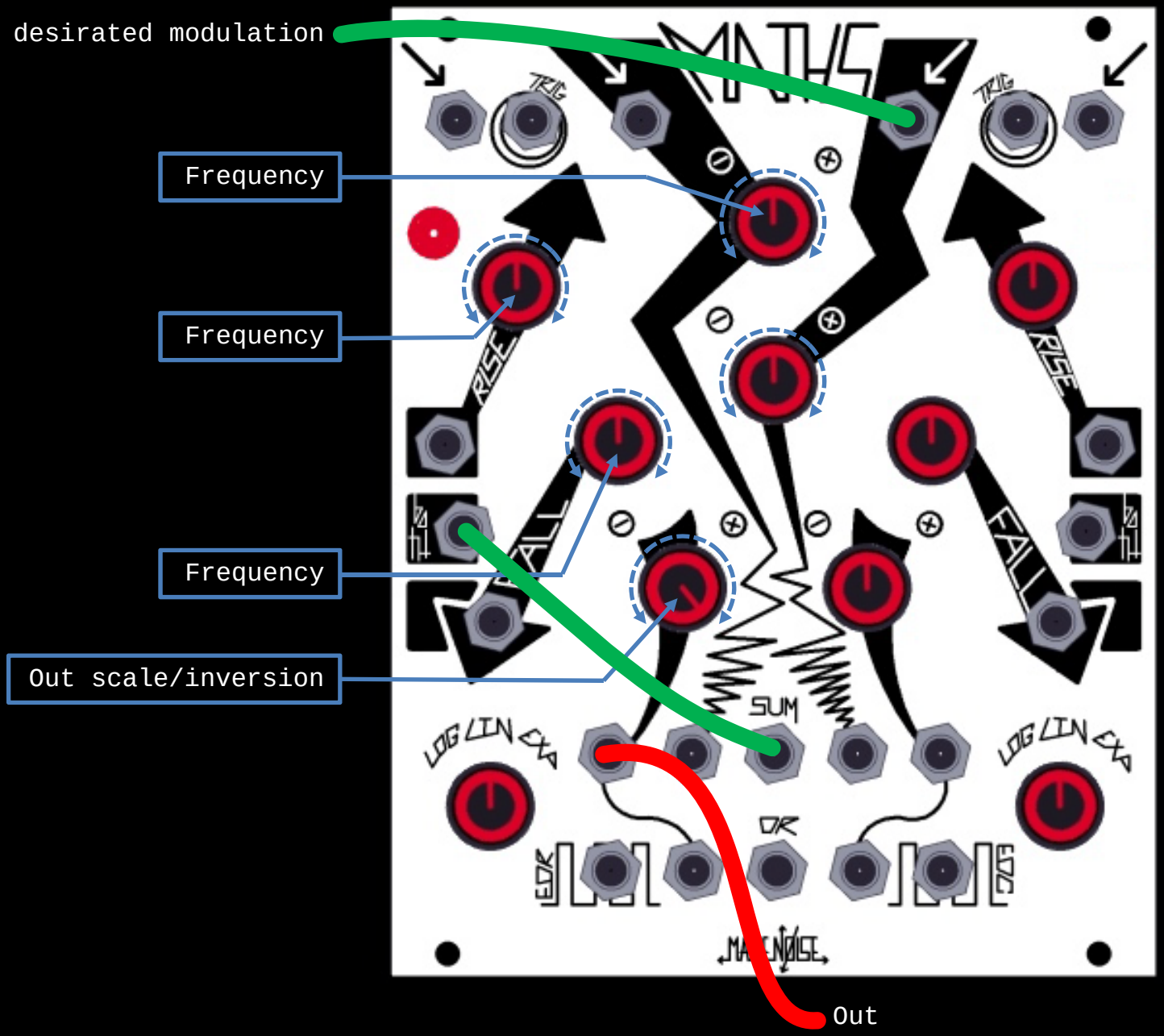
Voltage Controlled Transient Generator (Attack/ Decay EG)



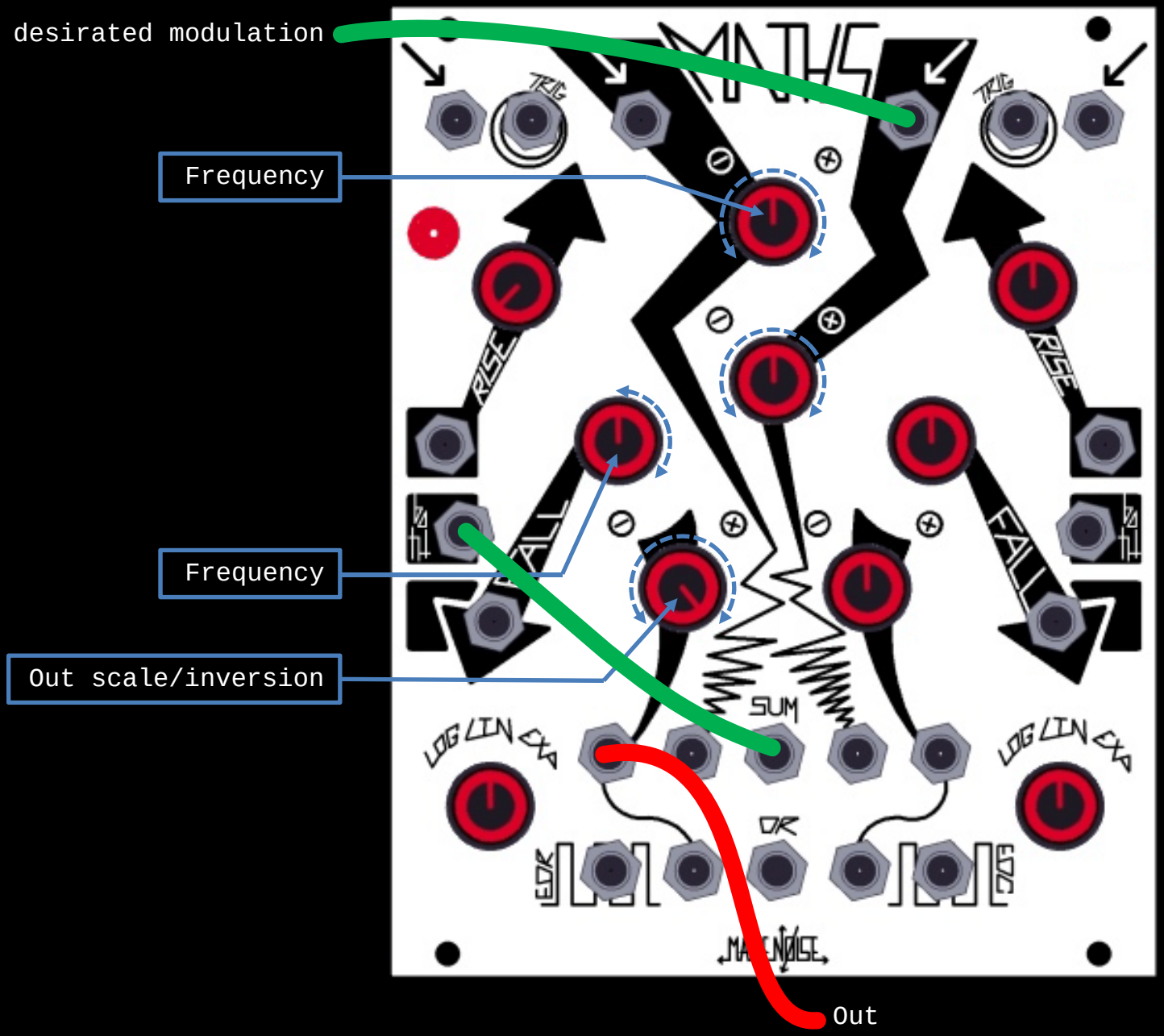
Voltage Controlled Sustained Function Generator (A/S/R EG)



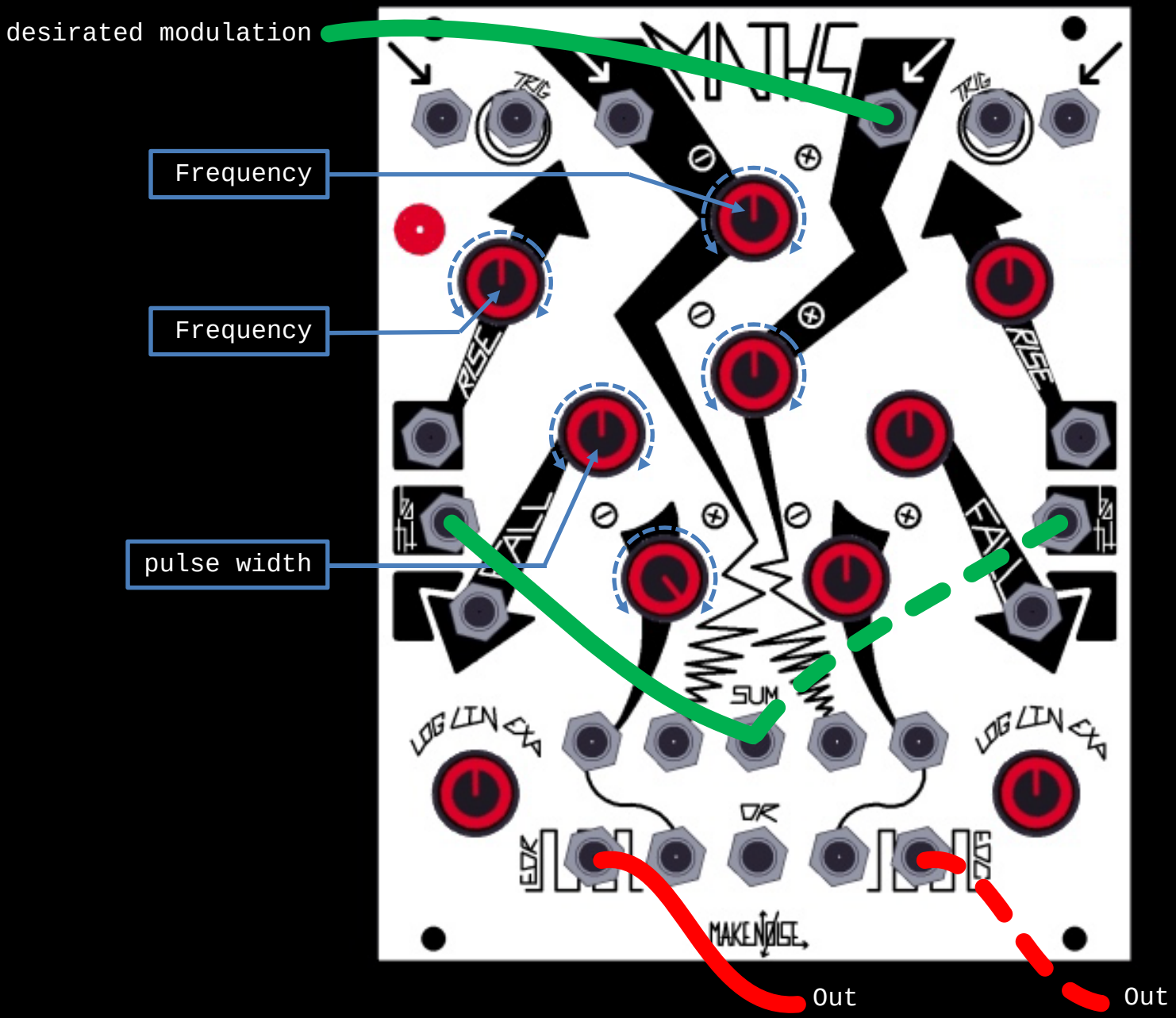
Typical Voltage Controlled Triangle Function



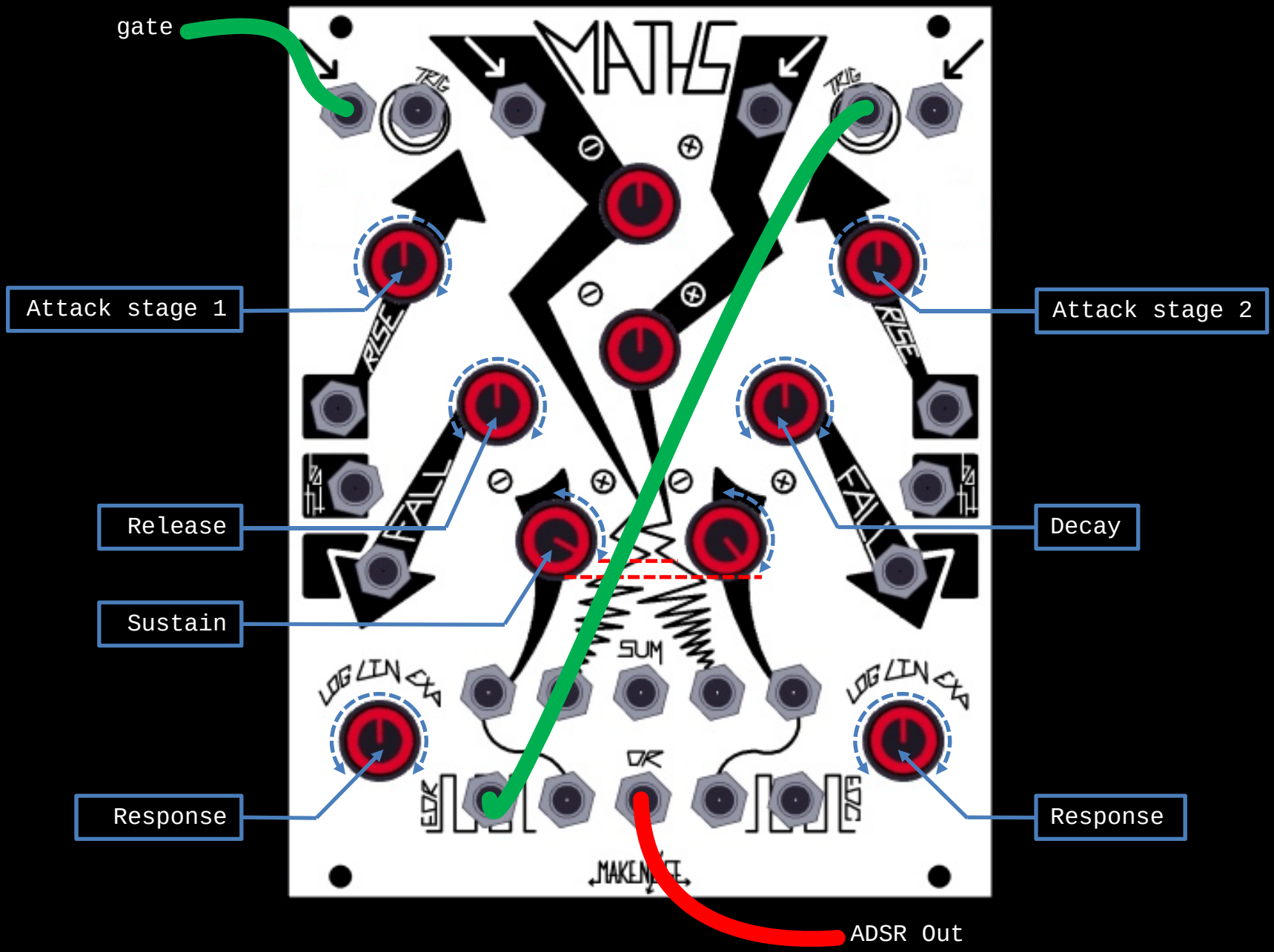
Typical Voltage Controlled Ramp Function



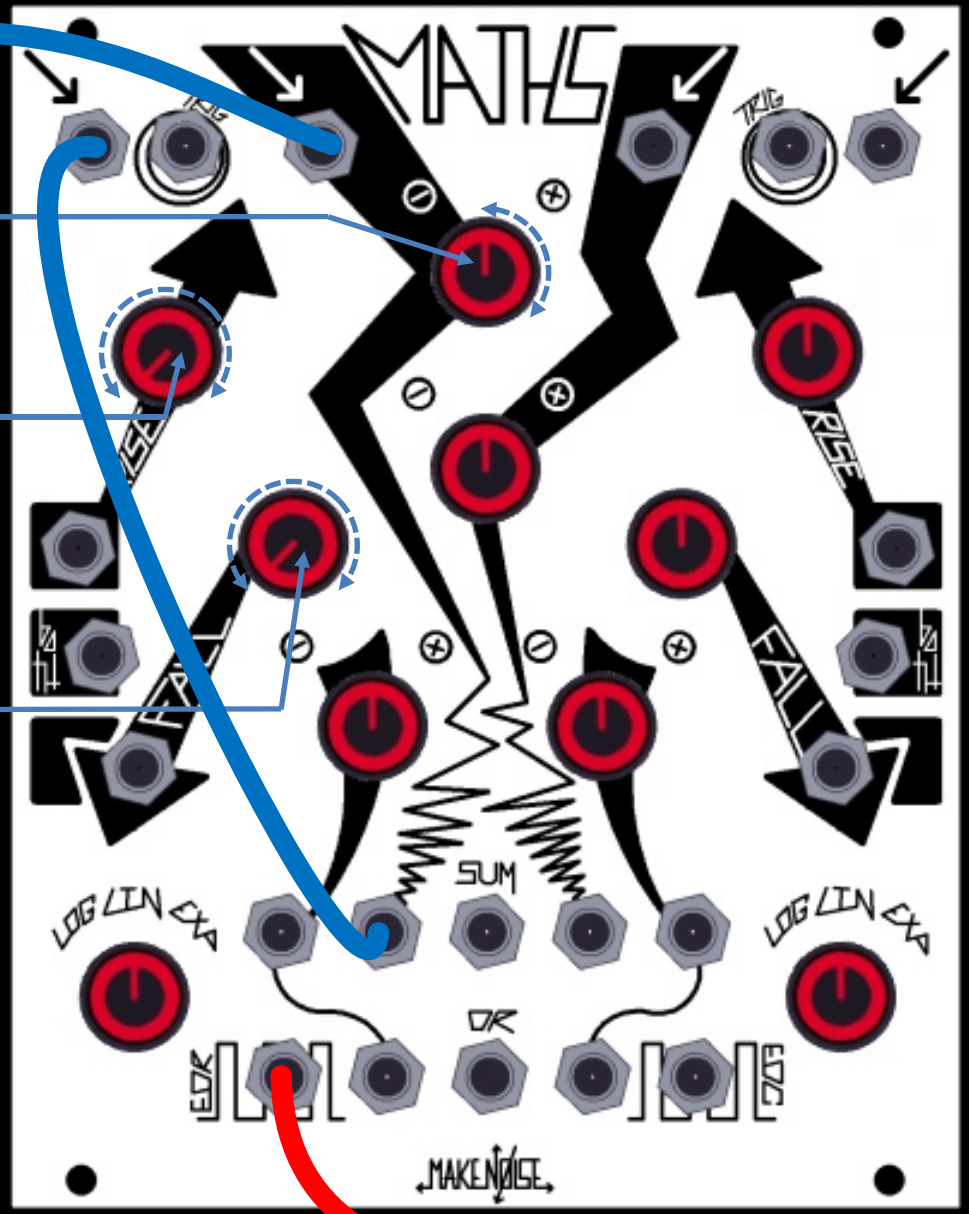
Typical Voltage Controlled Pulse



Voltage Controlled ADSR (East Coast Envelope done West Coast style)



Voltage Comparator/ Gate Extraction w/ variable width



signal to be compared

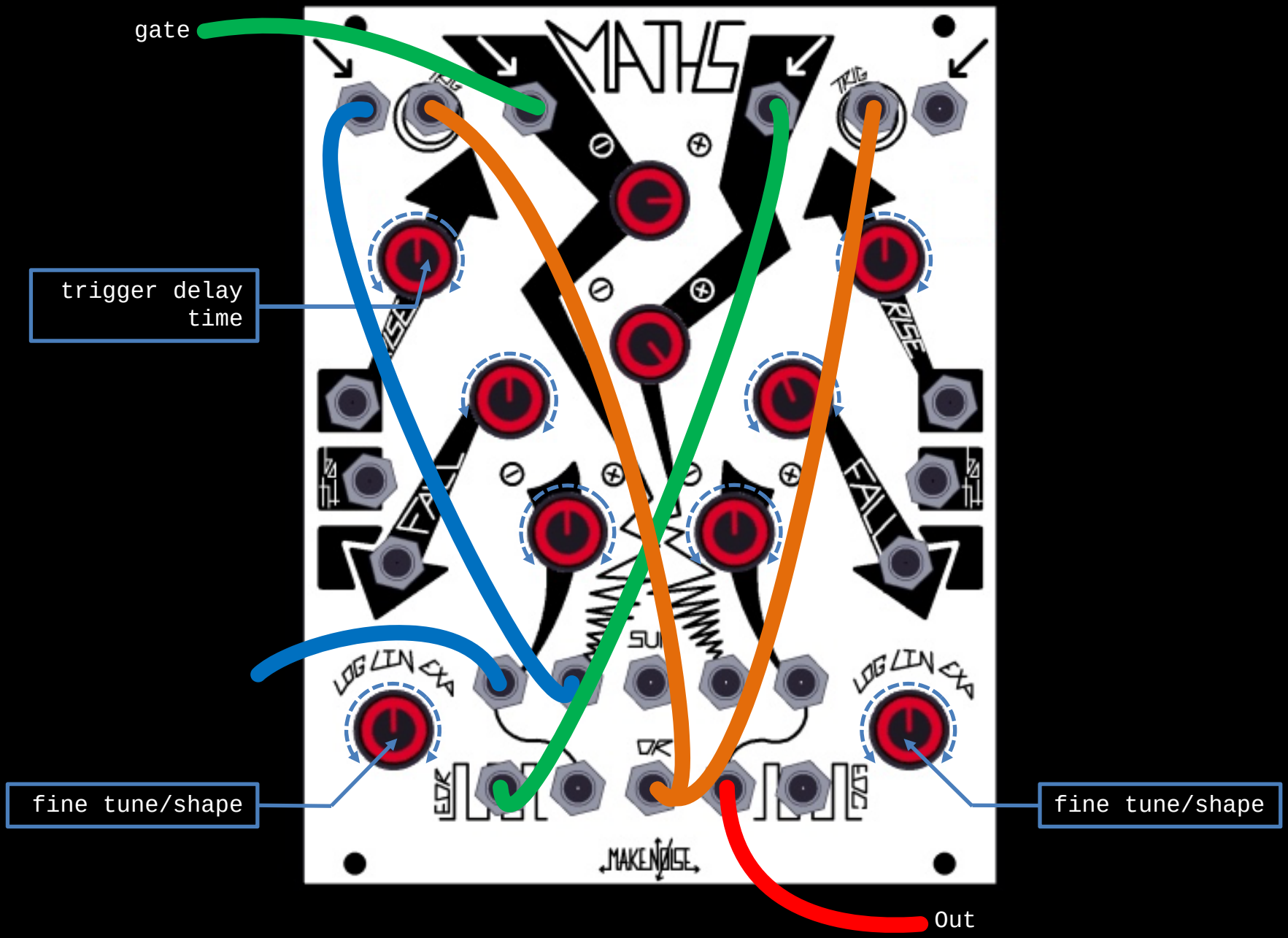
threshold

derivate gate Delay

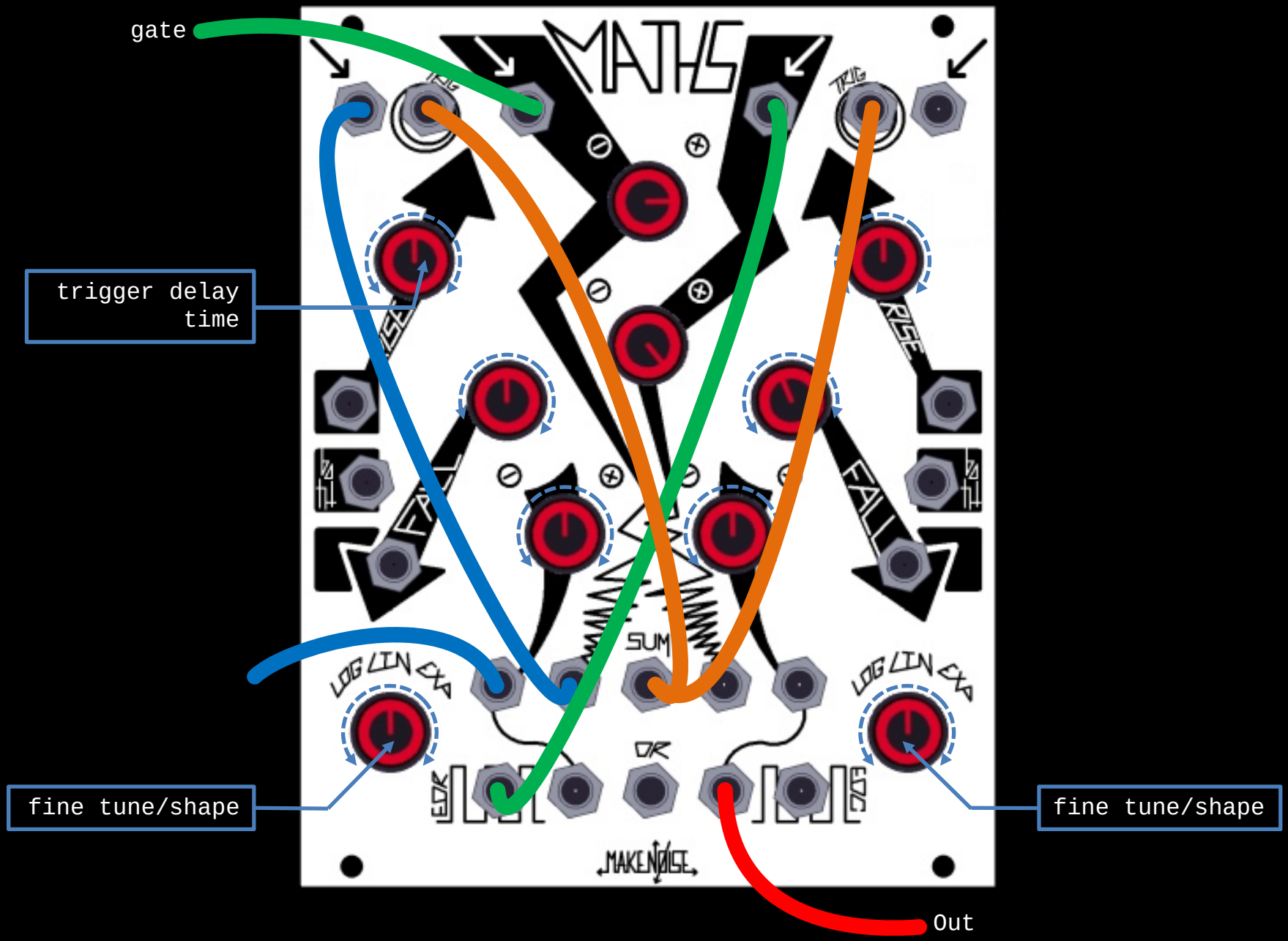
derivate gate Width

extracted gate

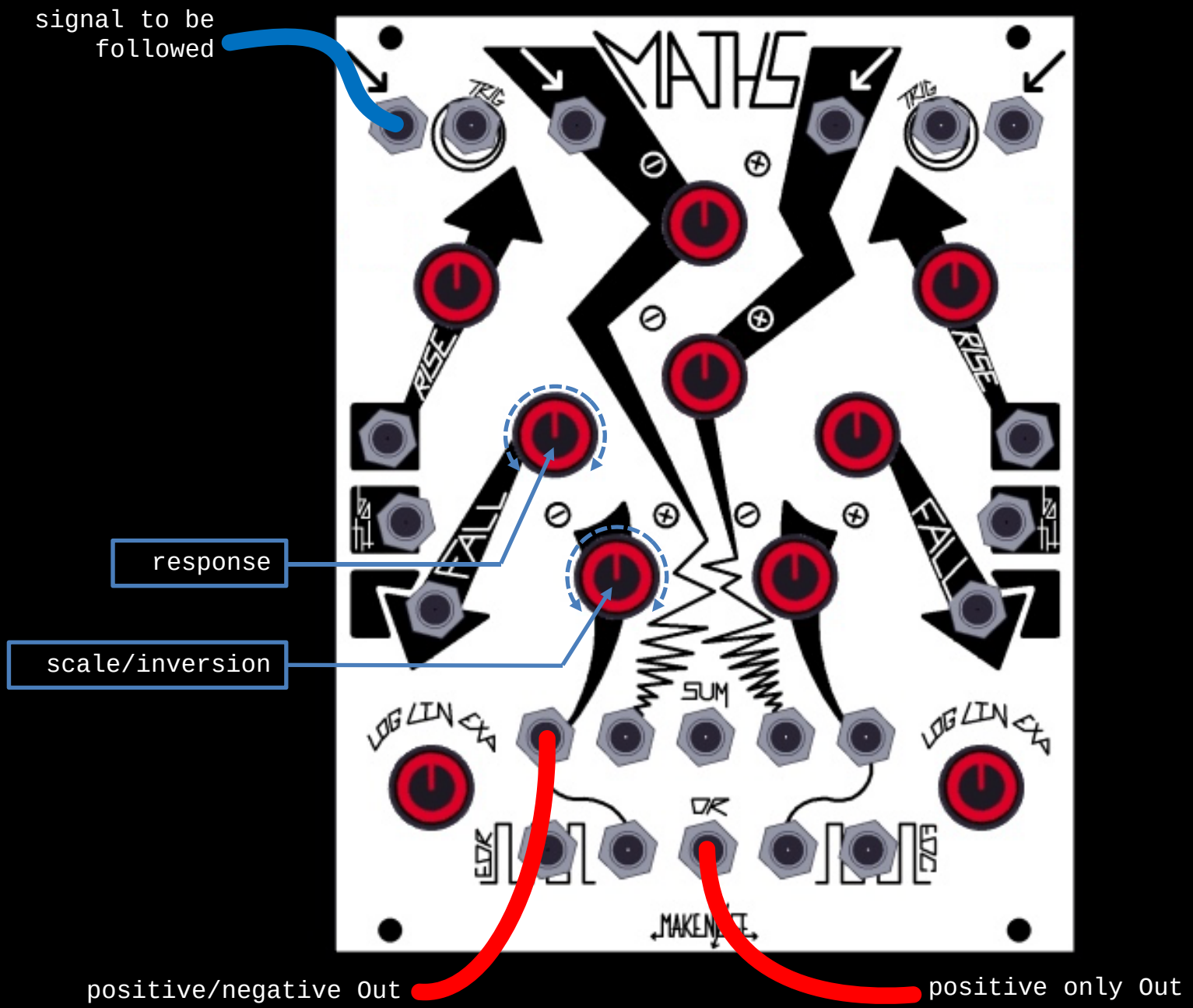
Gate Controlled CYCLE



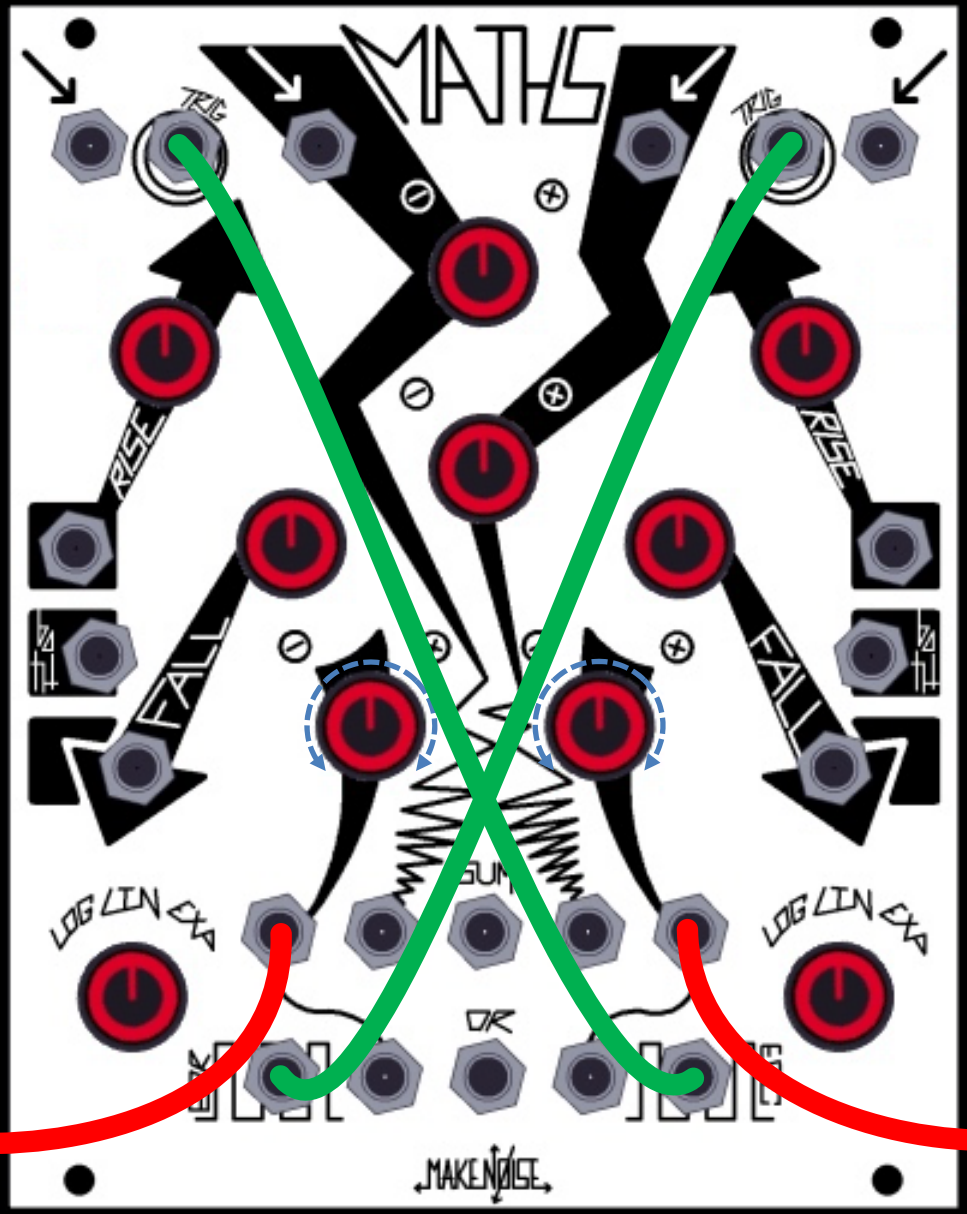
Toggled Delayed CYCLING



Envelope Follower



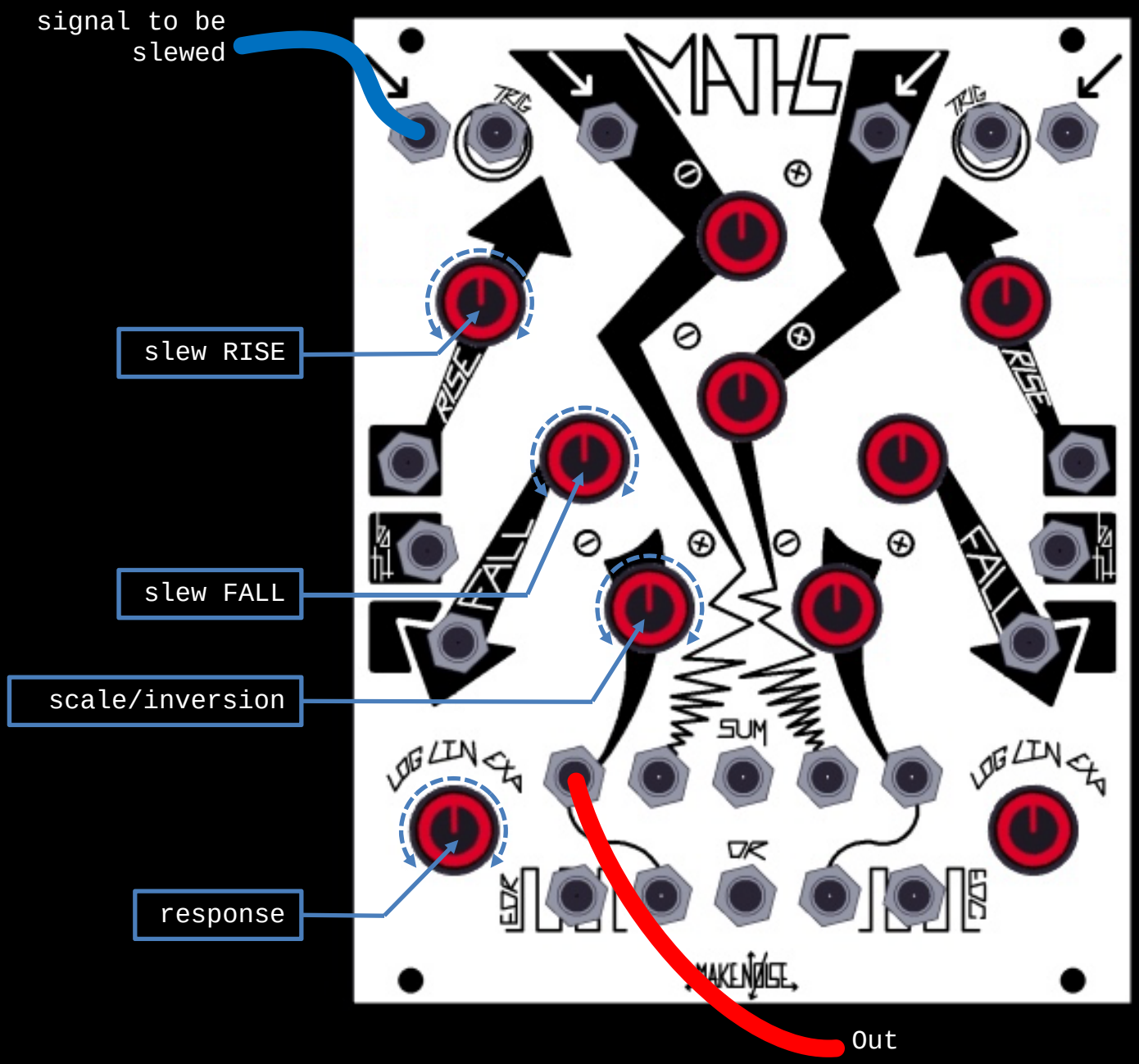
281 “Quadrature Mode” (West Coast Swirly Bird)



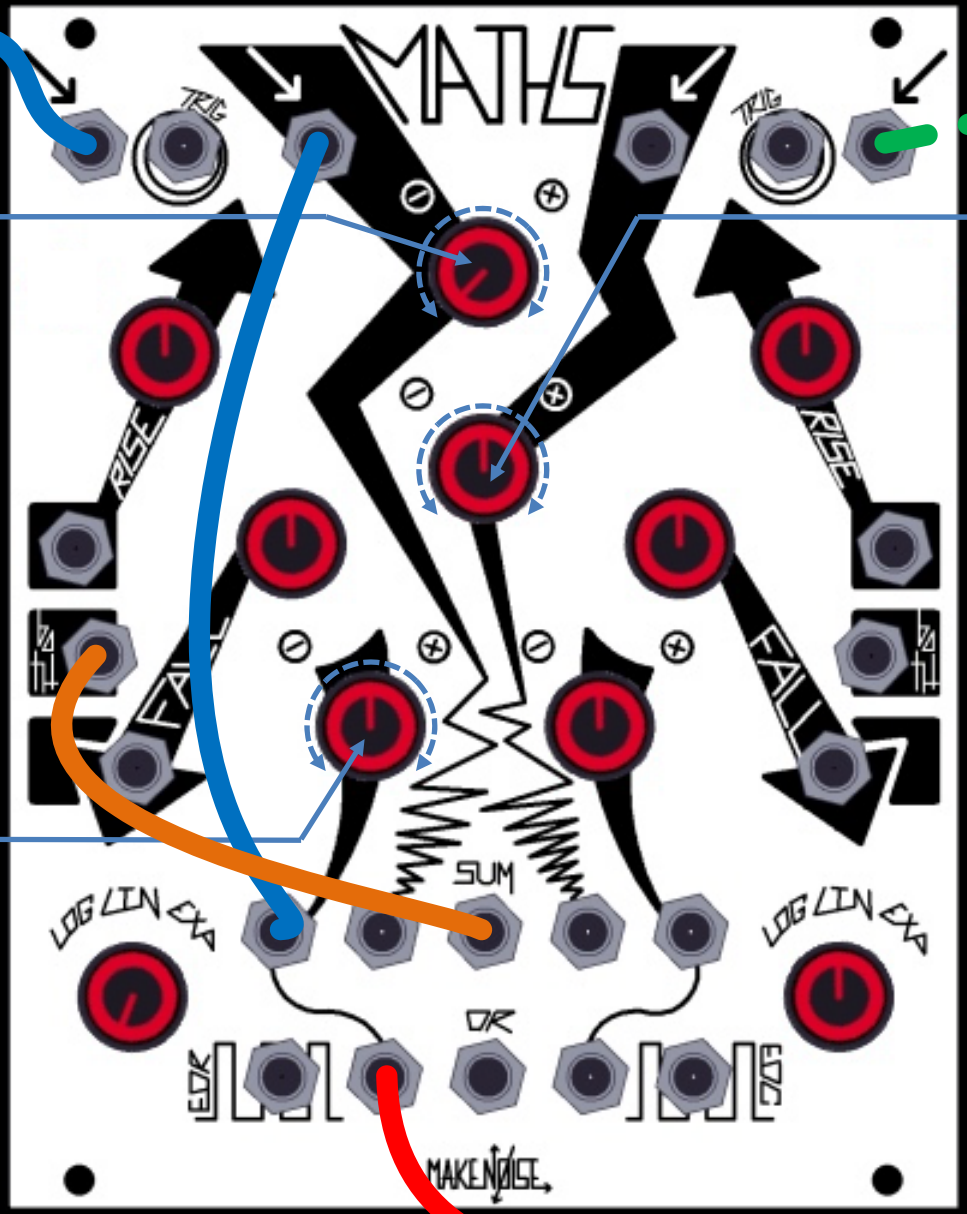
modulation destination

modulation destination

VC LAG/ Slew Processor



East Coast Portamento



signal to be slewed

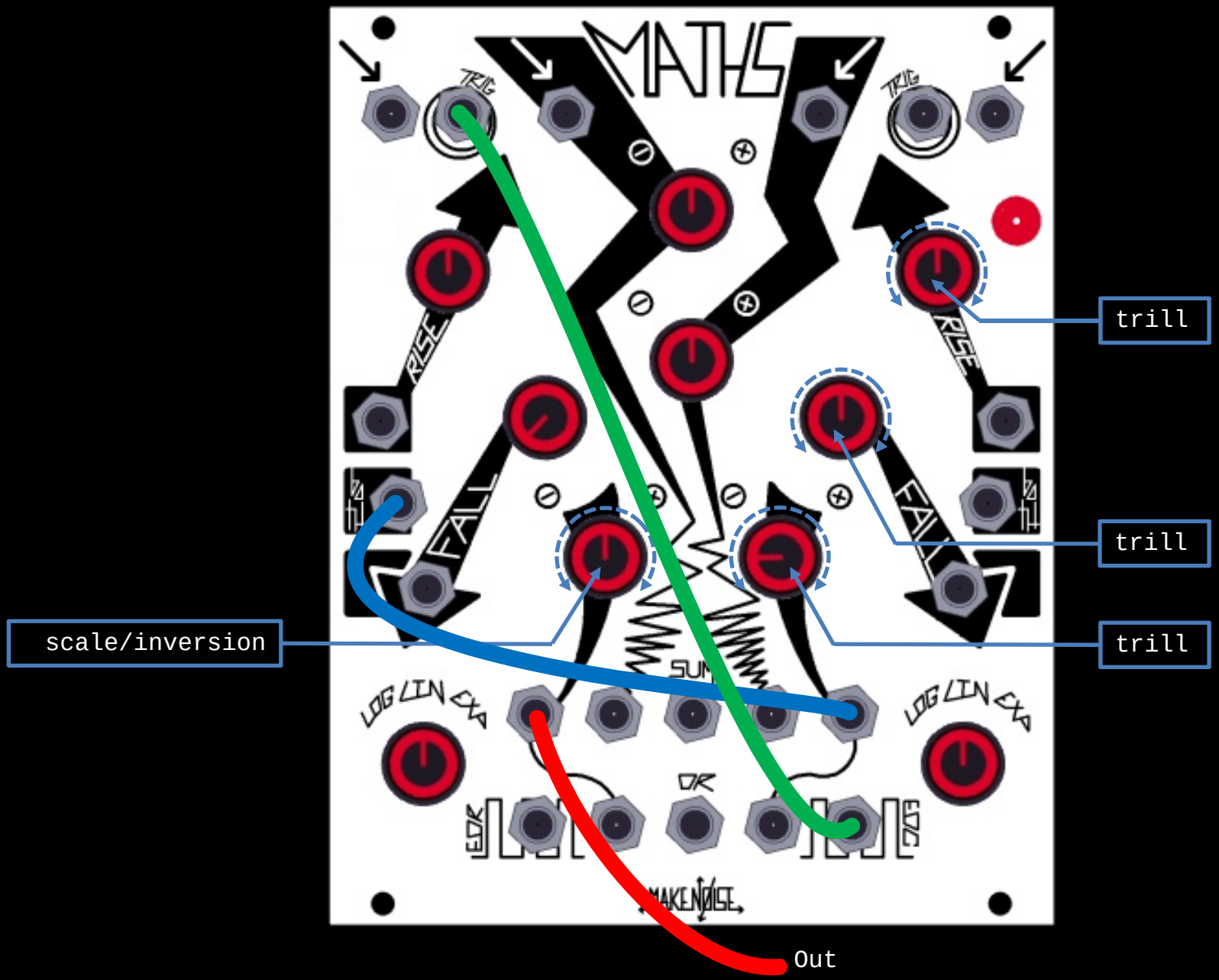
extremeness of the LOG response

portamento rate

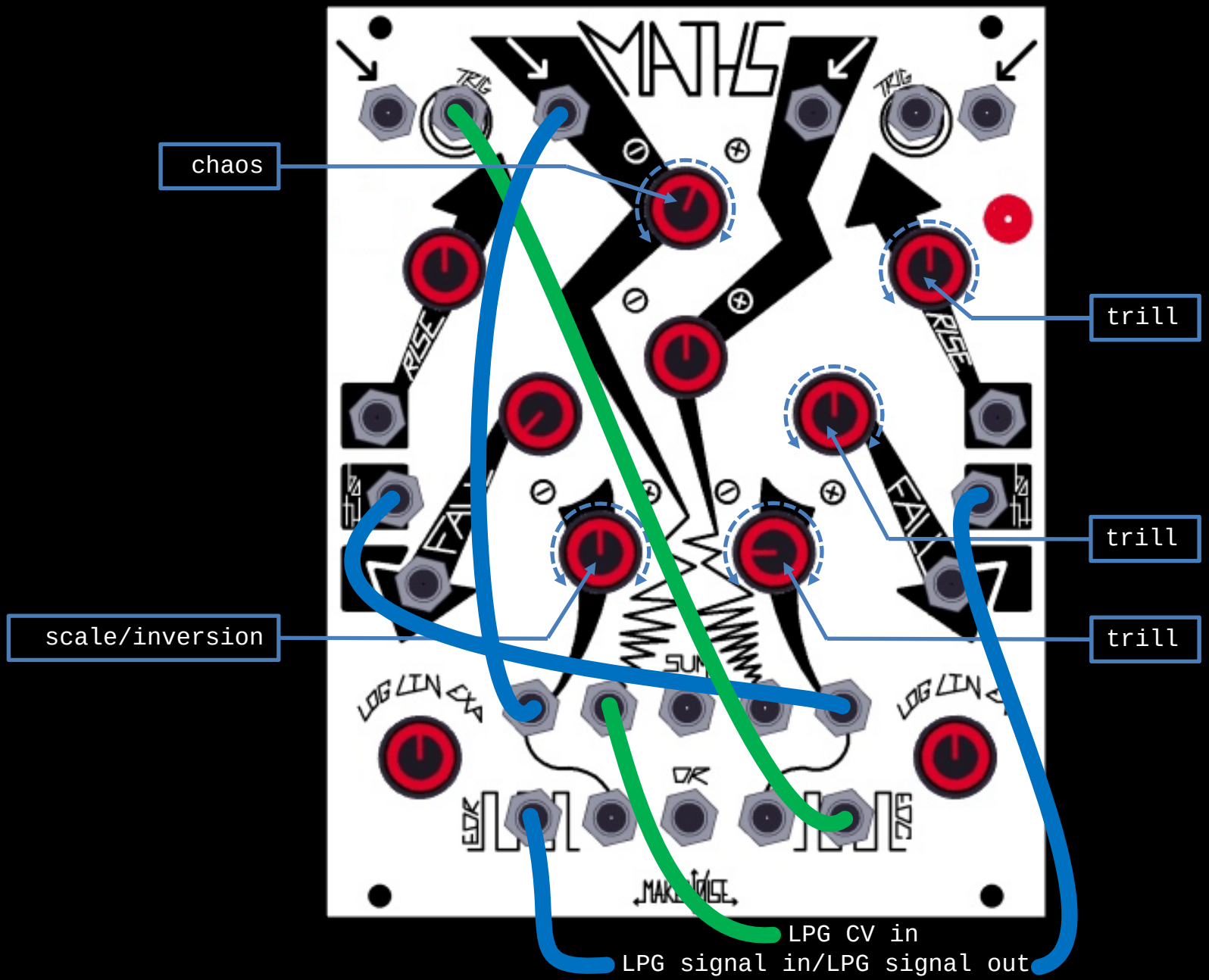
scale/inversion

Out

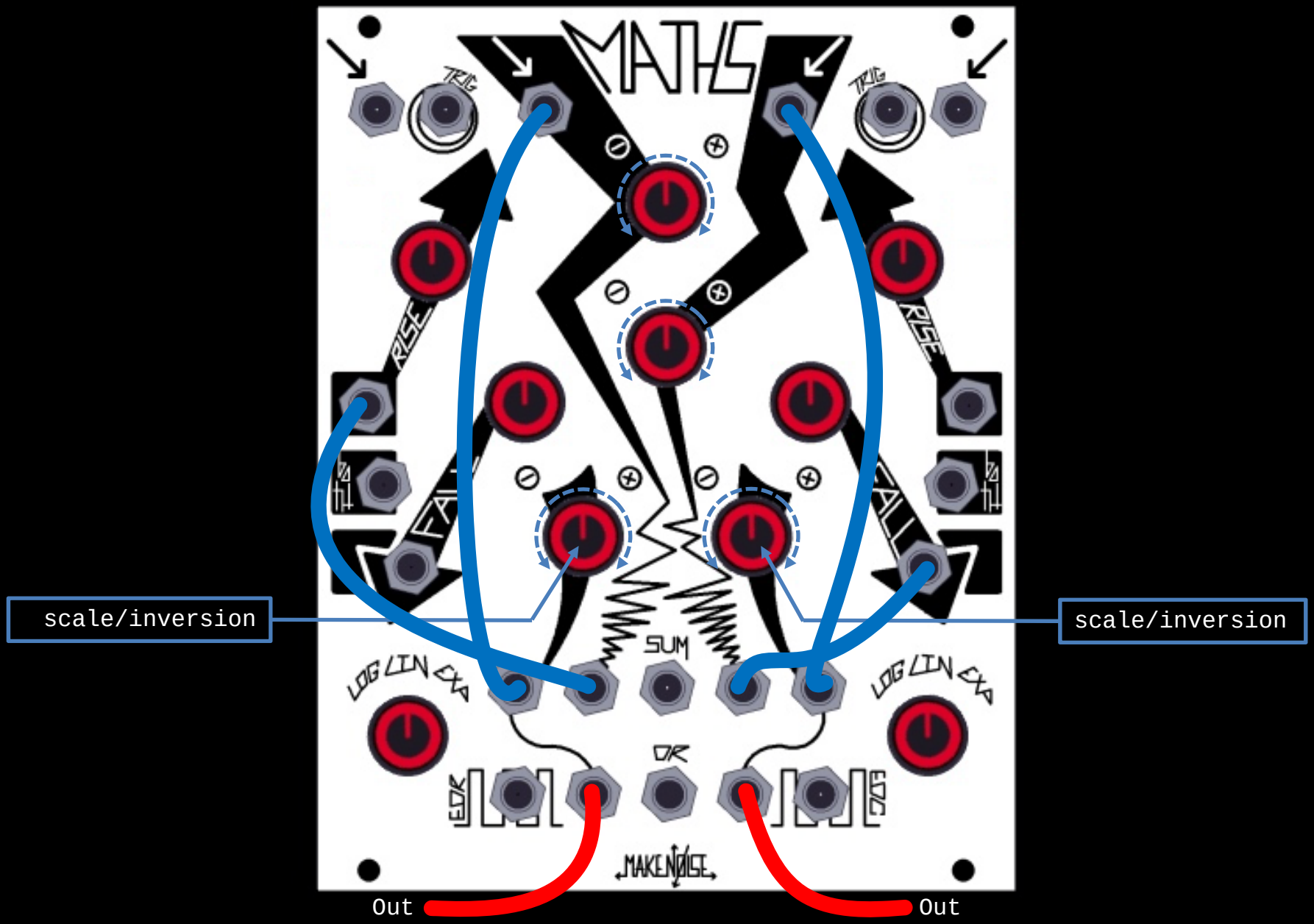
Arcade Trill



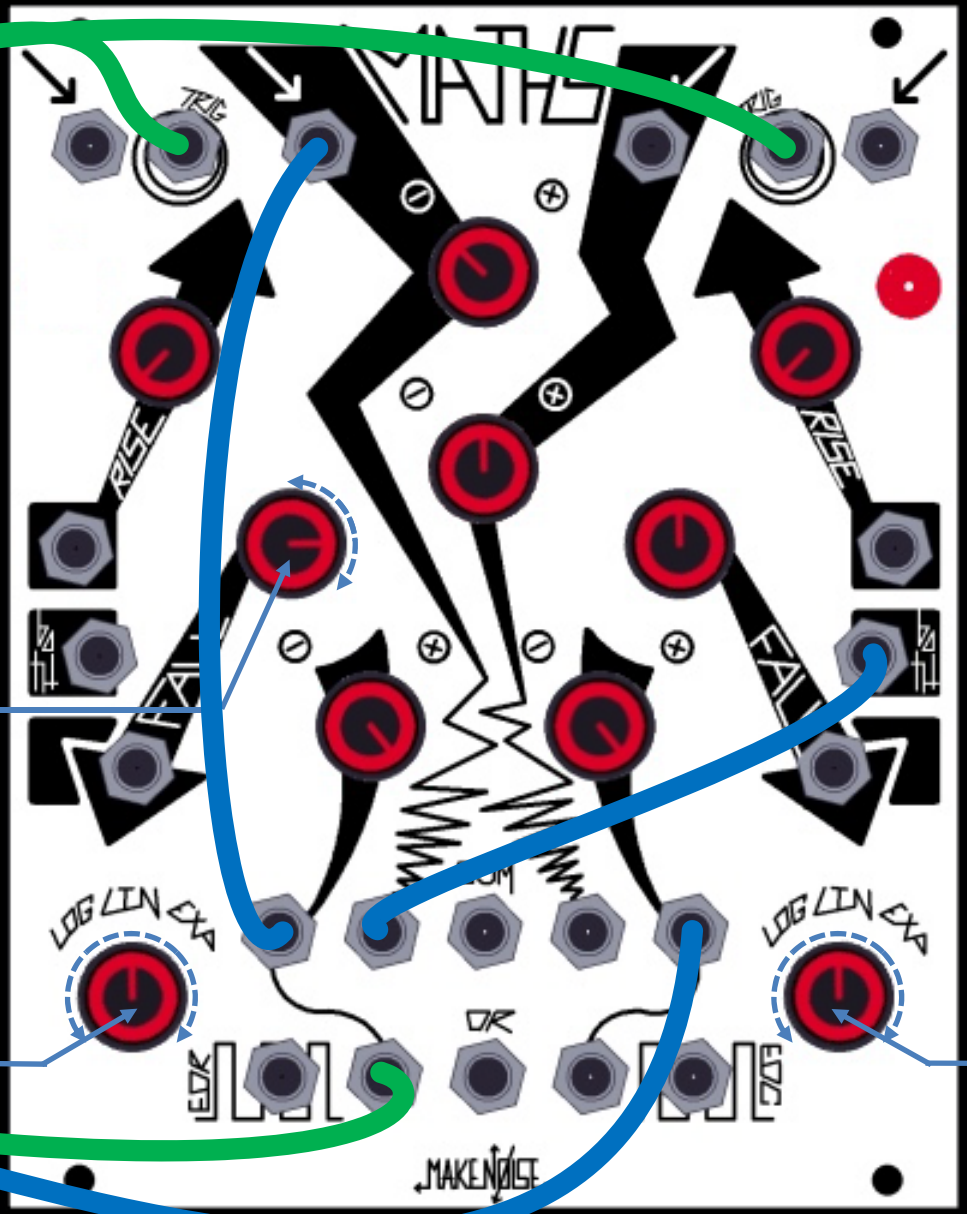
Chaotic Trill (requires QMMG or other Direct Coupled LP filter)



Independent Contours



Bouncing Ball (requires QMMG or Dual VCA)



gate/trigger

signal to be bounced

Restitution

Out

Gravity

Gravity

VCA 1 CV IN

VCA 1 signal IN

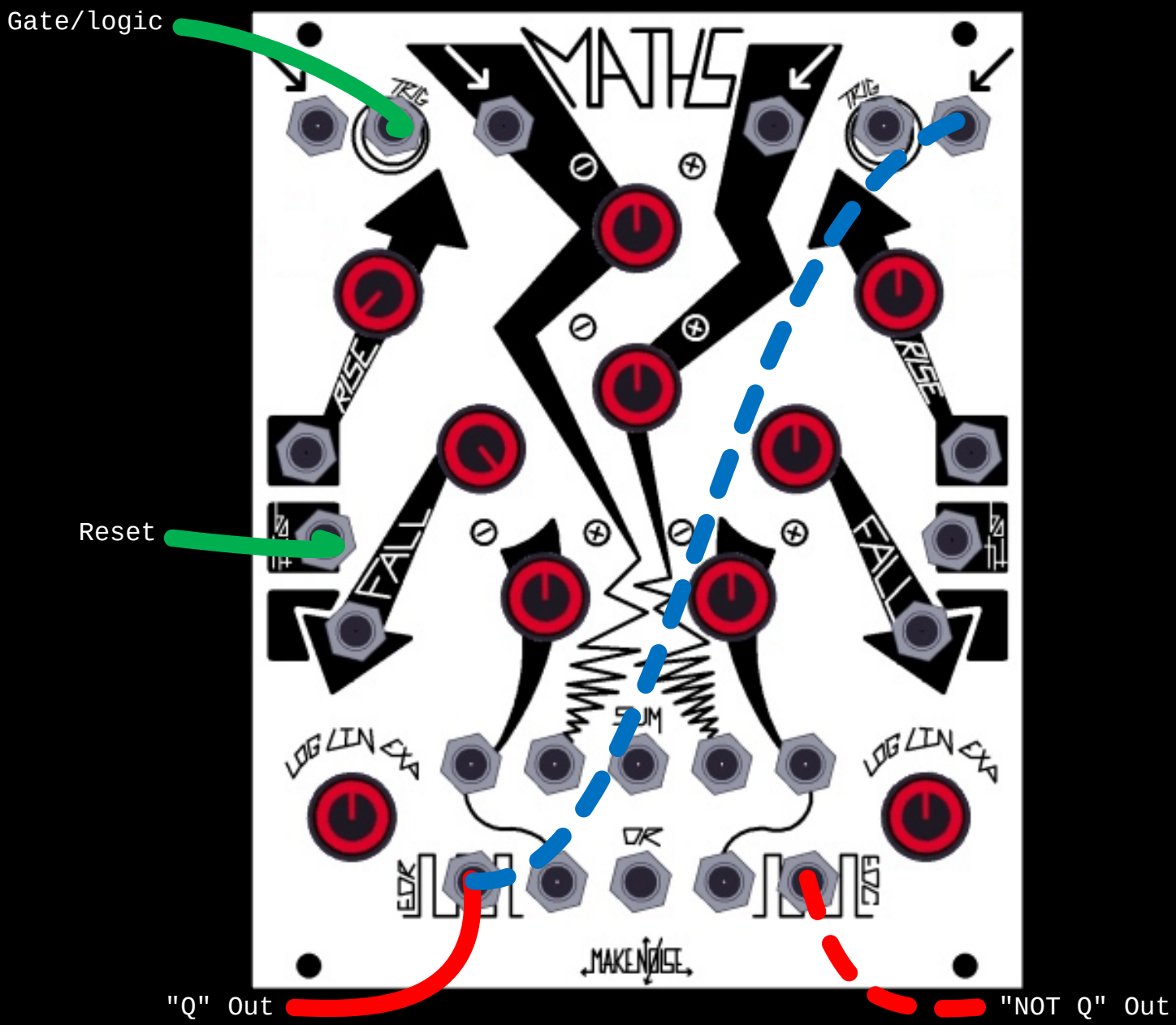
VCA 1 signal OUT

VCA 2 signal OUT

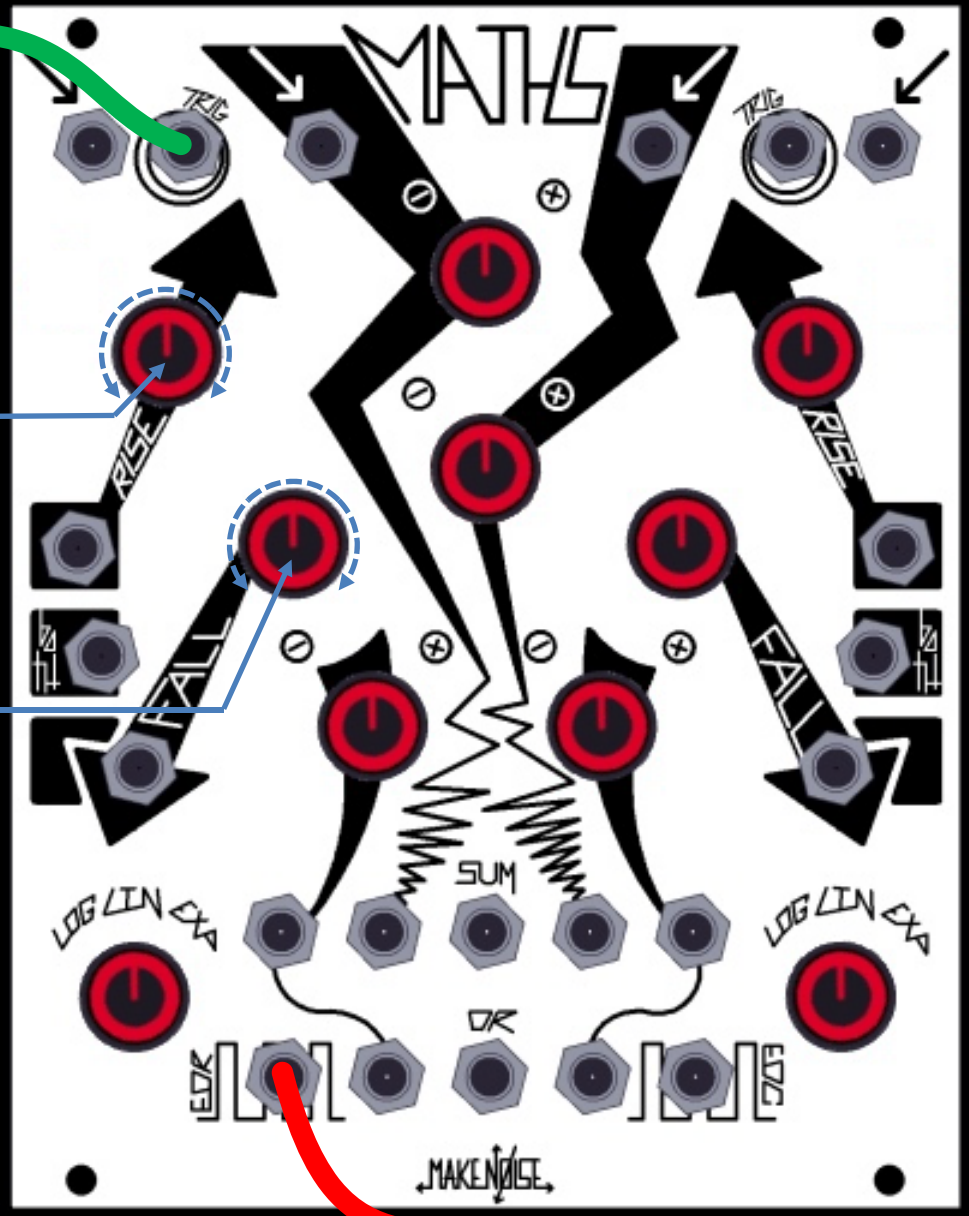
VCA 2 signal IN

VCA 2 CV IN

FLIP-FLOP (1-Bit Memory)



Voltage Controlled Pulse Delay Processor



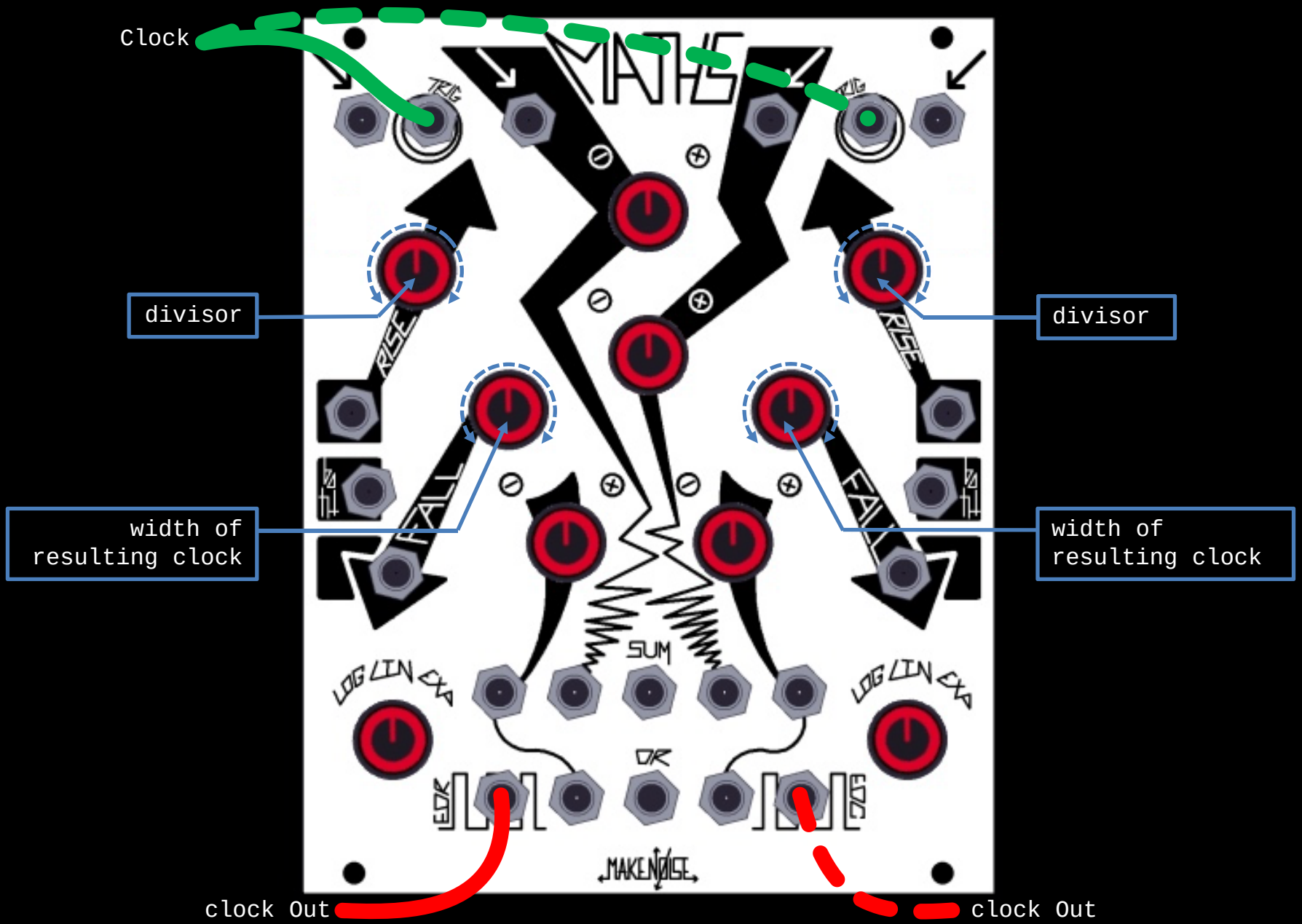
Trigger or Gate

delay

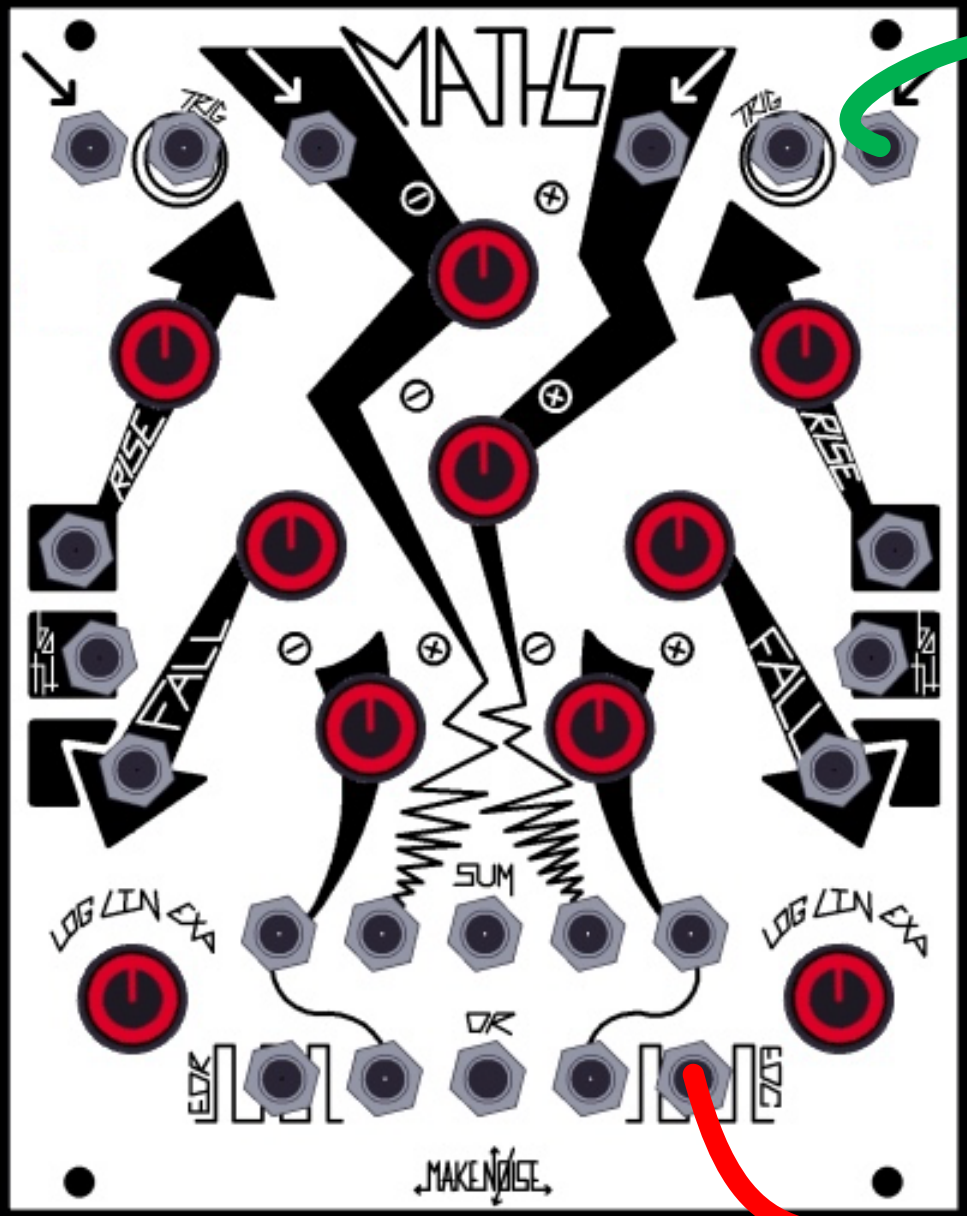
width of delayed pulse

Out

Voltage Controlled Clock Divider

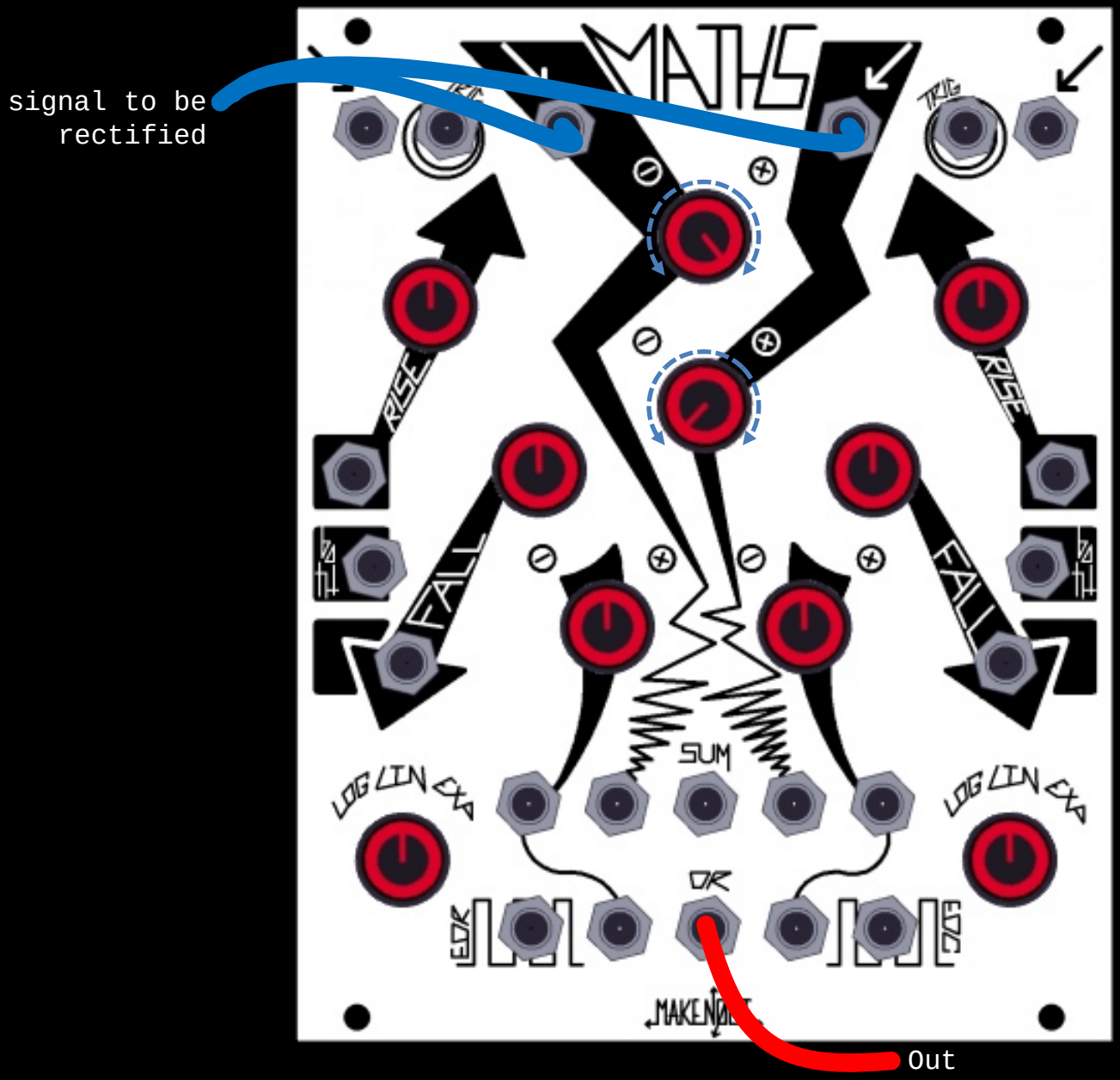


Logic Invertor

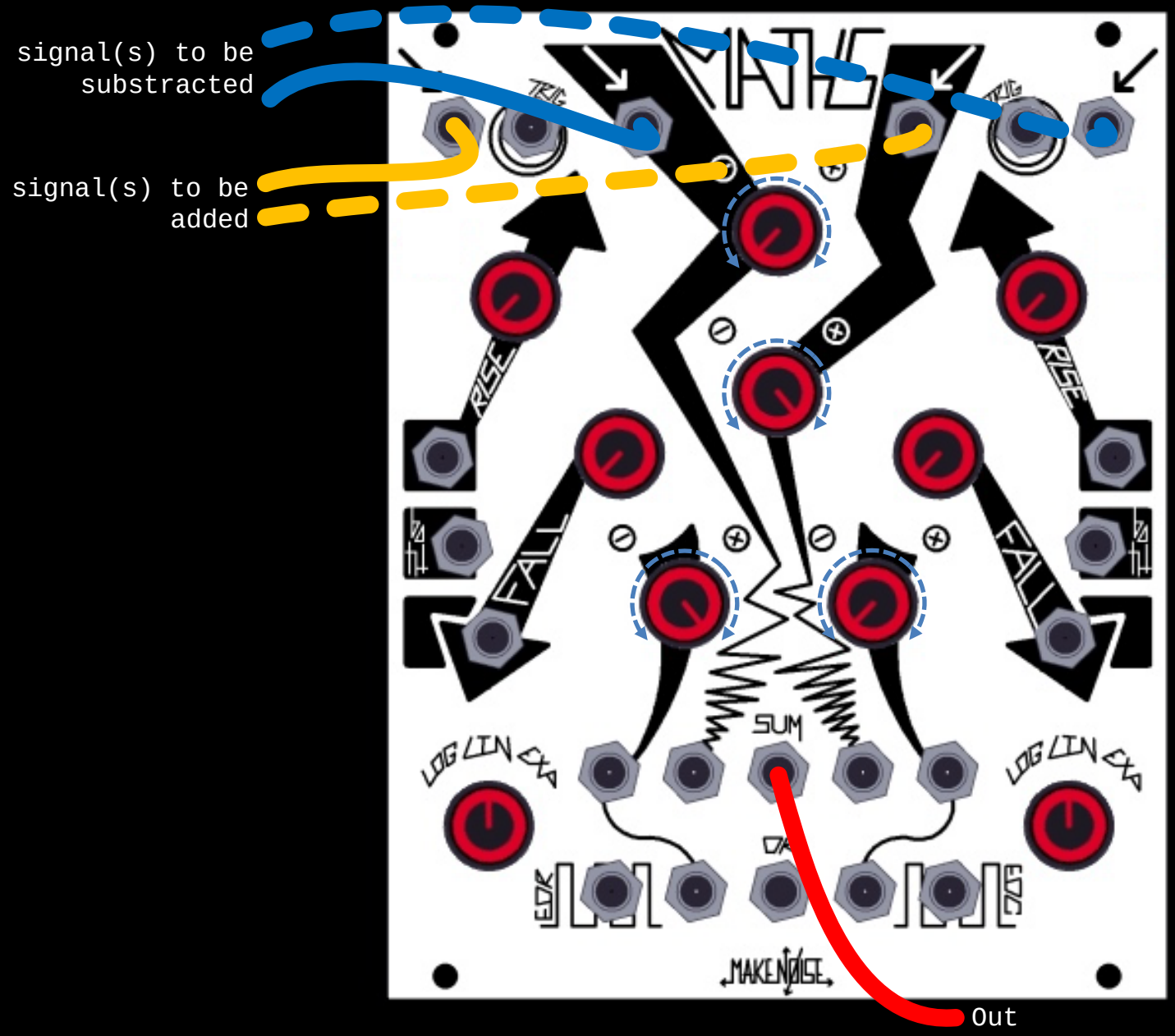


Half Wave Rectification

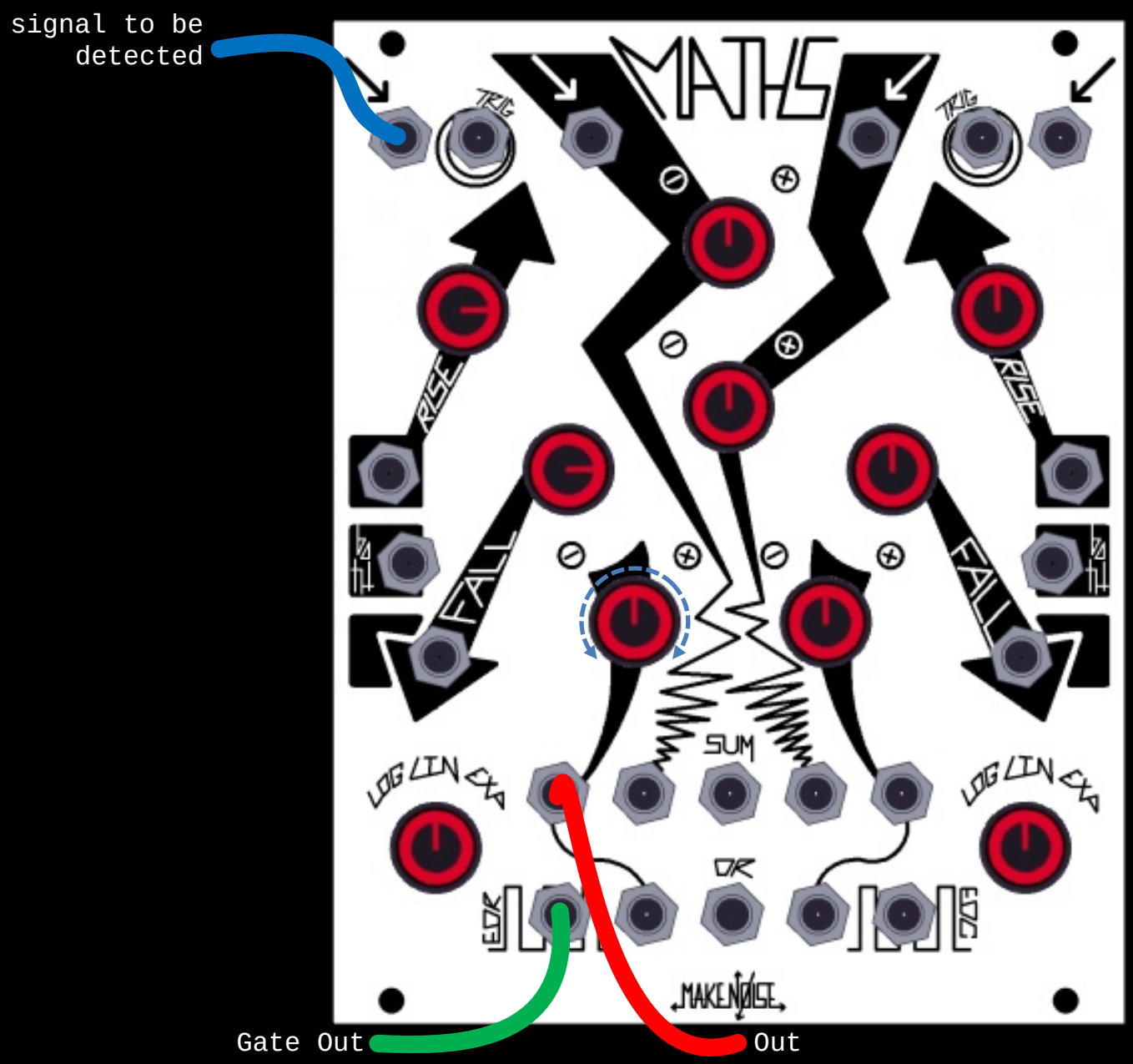
Full Wave Rectification



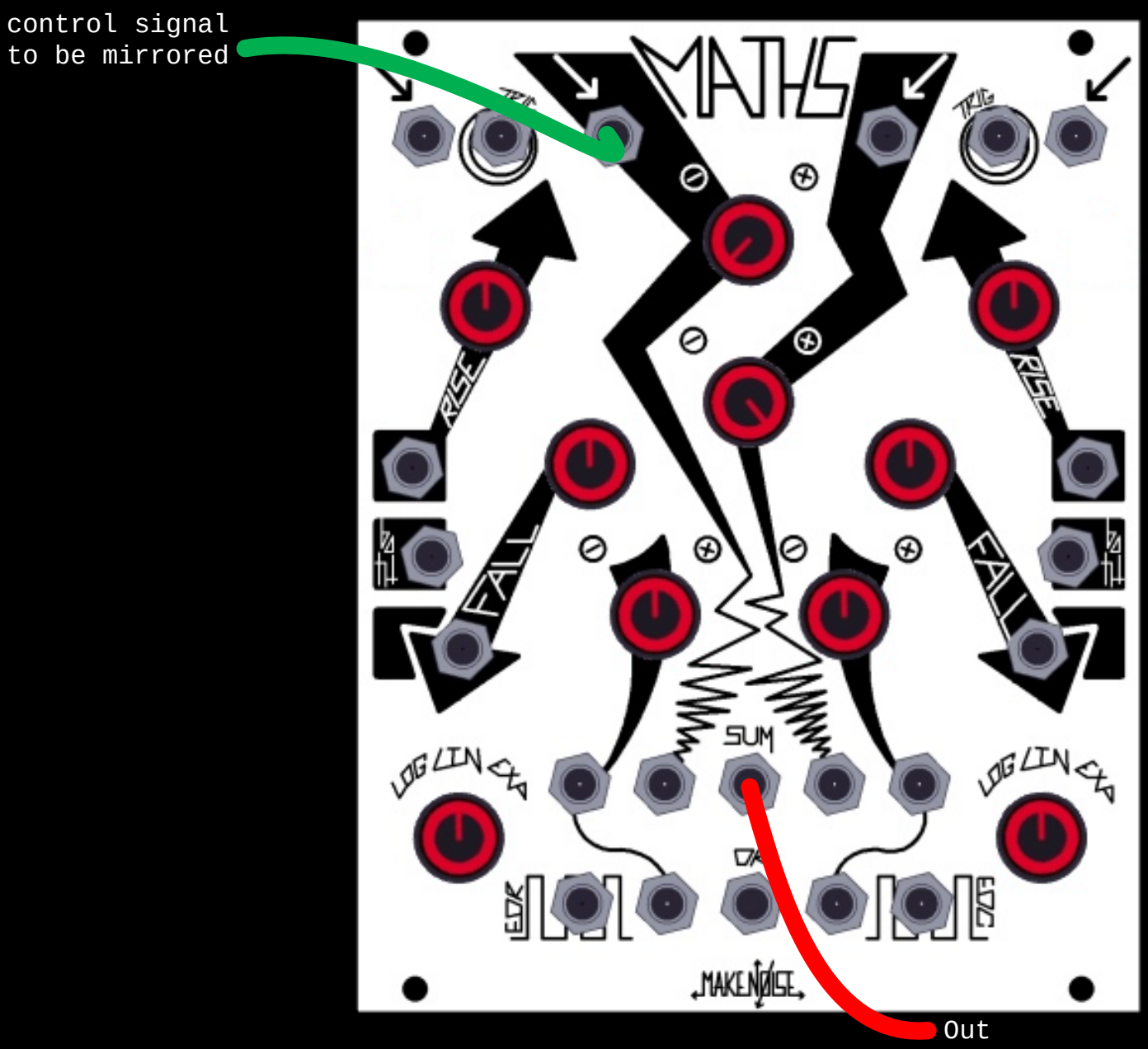
ADD, Subtract Control Signals



Peak Detector



Voltage Mirror



Multiplication